2 Graduate Research Assistantships on an NSF & SRC research project at the University of Central Florida

Probabilistic Spin Logic for Low-Energy Boolean and Non-Boolean Computing

Computer architecture GRA positions available:
- Spintronic Circuits and Architectures
- Neuromorphic / Brain-Inspired Approaches
- Reliable Hybrid/Post-CMOS Logic Design
- Reconfigurable and Adaptive Computing

Qualifications:
- Completed a Masters Degree in Computer or Electrical Engineering
- Research publications in-print at IEEE/ACM journals/conferences
- Research experience in CMOS logic design / computer architecture
- GRE (V+Q) > 308, TOEFL > 90, highest academics and work ethics
- Prefer 2 or more: SPICE, Verilog, Mumax³, Brian, OOMMF, MATLAB

Benefits:
- High pay-rate during Spring, Fall, and Summer semesters
- Tuition and fees paid in-full
- 2 undergraduate research assistants assigned to assist you
- New PC Workstation and server access
- Travel budget to top-rated conferences
- Funded fellowship opportunities to supplement pay even further

Advantages:
- Internationally-Recognized Computer Hardware Research Lab:
  - 8 best paper awards in last 3 years
  - Professional and fun team environment
  - Top career placement within academia (including ECE Department Chair and Dean), Oak Ridge National Lab, AMD, Apple, Qualcomm, etc.
  - Dr. Ronald F. DeMara is leading and caring Advisor with 43 M.S. thesis + Ph.D. graduated, 26 years as Professor, $10.9M funding, 250 publications
- Team of Purdue (CAPSL), Berkeley, and Univ of Minnesota (C-Spin)
- Beautiful campus, modern building, located in Orlando, Florida
- Rising reputation: our graduates' degrees are worth more every year
- Graduate in 3.5 years (lab average)

Apply for Spring 2019 Ph.D. Program Admission:
E-mail your resume / CV / best academic paper to:
ronald.demara@ucf.edu
before 15 June 2018 for full consideration!

Computer Architecture Lab
http://cal.ucf.edu
University of Central Florida