Computer Architecture Lab's Citings through 2019: Group Five

Arman Roohi, Webmaster
Computer Architecture Lab
University of Central Florida
Orlando, Florida 32816-2362
E-mail: aroohi@knights.ucf.edu

***Abstract* — Citations which have been made. yet were not indexed, are summarized herein. The citing document is listed and the cited articles are noted. These are compiled and thus indexed for rapid identification within both printed and electronically-formatted books. By checking each of the entries, the hyperlink may be followed to view the book and cited articles. Only items which are not proceedings volumes have been included. These entries may be readily obtained at books.google.com and/or oversea.cnki.net**

Keywords — Citing article, Citation index, Cited articles, Book citations.

1.0 Introduction

In this paper, the primary focus is to identify book citations for rapid retrieval. The paper provides a concise list of them that would not otherwise be available in a single document. Sources listed were obtained via web search and then filtered as indicated below. Searches included "R.F. DeMara" and "DeMara, R" as well as "R DeMara" which were then inspected manually for matching content.

2.0 Google Books and Related Citings

James Hereford and David Gwaltney authored a NASA Technical Report which seems to have improper pdf scanning but also published as a book on scalability, timing, and system design issues for intrinsic evolvable hardware in 2004 which cited [1].

The book by Muthukrishnan Senthilkumar and Vijayalakshmi Ramasamy authored on Computational Intelligence, Cyber Security and Computational Models published by Springer Publishers in 2013, cited [2-8] on page 164.

The book by Patrick Brézillon, and Avelino J. Gonzalez on *Context in computing: a cross-disciplinary approach for modeling the real world* published by Springer in 2014 cited [9] on page 507.

The book by Uma Shanker Tiwary on *Speech, Image, and Language Processing for Human Computer Interaction* published in 2012 cited [10] on page 146.

The graduate thesis "The Design of Asynchronous MCU Based on NCL" by Xiaojie Ming from Xi'an University of Electronic Science and Technology was published in 2013 which cited [11].

The graduate thesis “The Simulation Dynamic Process Assess on Single Dining-room and Optimization Research of Peak-cutting Strategy of Multiple Dining-rooms” by Xiaofeng Wang from Shanghai Jiaotong University was published in 2007 which cited [12].

3.0 Conclusion

Citations appear on the pages as mentioned. Based on the citations above, it is possible to rapidly locate the articles by google scholar search using the stated booked titles. The cited articles are listed as indicated.

References

[1] J. D. Lohn, G. Larchev, and R. F. DeMara, “Evolutionary Fault Recovery in a Virtex FPGA Using a Representation that Incorporates Routing,” in *Proceedings of the Seventieth International Parallel and Distributed Processing Symposium*, *Reconfigurable Architectures Workshop*, pp. 172, Nice, France, April 22-26, 2003.

[2] R. S. Oreifej, C. A. Sharma, R. F. DeMara, "Expediting GA-Based Evolution Using Group Testing Techniques for Reconfigurable Hardware," in *Proceedings of the IEEE International Conference on Reconfigurable Computing and FPGAs*, pp. 106 – 113, San Luis Potosi, Mexico, September 20 – 22, 2006.

[3] R. Al-Haddad, R. Oreifej, R. A. Ashraf, and R. F. DeMara, “Sustainable Modular Adaptive Redundancy Technique Emphasizing Partial Reconfiguration for Reduced Power Consumption,” *International Journal of Reconfigurable Computing*, Article ID 430808, June, 2011, pp 1 – 25, 2011. doi:10.1155/2011/430808.

[4] R. F. DeMara and K. Zhang, “Autonomous FPGA Fault Handling through Competitive Runtime Reconfiguration,” in *Proceedings of the NASA/DoD Conference on Evolvable Hardware*, pp. 109 – 116, Washington D.C., U.S.A., June 29 – July 1, 2005.

[5] K. Zhang, R. F. DeMara, C. A. Sharma, “Consensus-based Evaluation for Fault Isolation and On-line Evolutionary Regeneration,” in *Proceedings of the International Conference in Evolvable Systems*, pp. 12 – 24*,* Barcelona, Spain, September 12 – 14, 2005.

[6] R. F. DeMara, K. Zhang, and C. A. Sharma “Autonomic Fault-Handling and Refurbishment Using Throughput-Driven Assessment,” *Applied Soft Computing,* Volume 11, Issue 2, March 2011, pp. 1588 – 1599.

[7] R. S. Oreifej, R. N. Al-Haddad, H. Tan, and R. F. DeMara, “Layered Approach To Intrinsic Evolvable Hardware Using Direct Bitstream Manipulation Of Virtex II Pro Device,” in *Proceedings of the 17th International Conference On Field Programmable Logic And Applications*, Amsterdam, Netherlands, August 27 – 29, 2007.

[8] R. A. Ashraf and R. F. DeMara, “Scalable FPGA Refurbishment using Netlist-driven Evolutionary Algorithms,” *IEEE Transactions on Computers*, vol. 62, no. 8, pp. 1526 – 1541, August 2013. DOI:10.1109/TC.2013.58

[9] H. Fernlund, A. J. Gonzalez, R. F. DeMara, and M. Georgiopoulos, “Learning Tactical Human Behavior through Observation of a Human Actor,” *IEEE Transactions on Systems, Man and Cybernetics: Part B - Cybernetics*, Vol. 36, No. 1, February 2006, pp. 128 – 140.

[10] V. Hung, A. Gonzalez, and R. F. DeMara, “Towards a Context-Based Dialog Management Layer for Expert Systems,” in *Proceedings of the International Conference on Information, Process, and Knowledge Management*, Cancun, Mexico, February 2 – 7, 2009, pp. 60 – 65.

[11] S. C. Smith, R. F. DeMara, J. S. Yuan, D. Ferguson, and D. Lamb, “Optimization of NULL Convention Self-timed Circuits,” *Integration, The VLSI Journal*, Vol. 37, No. 3, August, 2004, pp. 135 – 165.

[12] H. A. Bahr and R. F. DeMara, “Smart Priority Queue Algorithms for Self-optimizing Event Storage,” *Simulation Modeling Practice and Theory*, Vol. 12, No. 1, April, 2004, pp. 15 – 40.