

Computer Architecture Lab's Citings through 2017: Group Four

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Abstract — Citations made yet not indexed are summarized. The citing document is listed and the cited articles are noted. These are compiled and thus indexed for rapid identification within both printed and electronically-formatted books. By checking each of the entries, the hyperlink may be followed to view the book and cited articles. Only items which are not proceedings volumes have been included. Later, this document was updated in 2019 for a new entry found in books.google.com

Keywords — *Citing article, Citation index, Cited articles, Book citations.*

1.0 Introduction

In this paper, the primary focus is to identify book citations for rapid retrieval. The paper provides a concise list of them that would not otherwise be available in a single document. Sources listed were obtained via web search and then filtered as indicated below. Searches included “R.F. DeMara” and “DeMara, R” as well as “R DeMara” which were then inspected manually for matching content.

2.0 Google Books and Related Citings

The book by Gottschalk, Petter. Investigation and prevention of financial crime: Knowledge management, intelligence strategy and executive leadership. CRC Press, 2016, cited [1] on page 250.

The book by Bourbakis, Nikolaos G. Artificial Intelligence Methods and Applications. Vol. 1. World Scientific, 1992, cited [2] on page 631, respectively.

The pdf document by Jason J. Gu, “Technical English,” Electrical and Computer Engineering, Dalhousie University, School of Control Science, November, 2006, cited [3-10] on pages 87-89.

Pauline Haddow and Andy Tyrrell authored “Evolvable Hardware Challenges: Past, Present and the Path to a Promising Future” which appeared in the book “Inspired by Nature: Essays Presented to Julian F. Miller on the Occasion of his 60th Birthday” edited by Susan Stepney and Andrew Adamatzky, Eds. which cited [11].

3.0 Conclusion

Citations appear on the pages as mentioned. Based on the citations above, it is possible to rapidly locate the articles by google scholar search using the stated booked titles. The cited articles are listed as indicated.

References

- [1] R. C. Watkins, K. M. Reynolds, R. F. DeMara, M. Georgiopoulos, A. J. Gonzalez, and R. Eaglin, "Tracking dirty proceeds: Exploring data mining technologies as tools to investigate money laundering," *Journal of Policing Practice and Research: An International Journal*, Vol. 4, No. 2, January, 2003, pp. 163 – 178.
- [2] R. F. DeMara and D. I. Moldovan, "The SNAP-1 Parallel AI Prototype," *IEEE Transactions on Parallel and Distributed Systems*, Vol. 4, No. 8, August, 1993, pp. 841 – 854.
- [3] B. S. Motlagh and R. F. DeMara, "Performance of Scalable Shared-Memory Architectures," *Journal of Systems, Circuits, and Computers*, Vol. 10, No. 1, February, 2000, pp. 1 – 20.
- [4] S. C. Smith, R. F. DeMara, J. S. Yuan, M. Hagedorn, and D. Ferguson, "Delay-Insensitive Gate-level Pipelining," *Integration, The VLSI Journal*, Vol. 30, No. 2, November, 2001, pp. 103 – 131.
- [5] J. Castro, M. Georgiopoulos, and R. F. DeMara, "Data-Partitioning using the Hilbert Space Filling Curves: Effect on the Speed of Convergence of Fuzzy ARTMAP for Large Database Problems," *Neural Networks*, Vol. 18, No. 7, September, 2005, pp. 967 – 984.
- [6] M. Georgiopoulos, J. Castro, A. Wu, R. F. DeMara, E. Gelenbe, A. J. Gonzalez, M. Kysilka, and M. Mollaghasemi, "CRCDD in Machine Learning at the University of Central Florida: Preliminary Experiences," in *Proceedings of the Eight Annual Conference on Innovation and Technology in Computer Science Education*, pp. 249, Thessaloniki, Greece, June 30 – July 2, 2003.
- [7] A. Ejnoui and R. F. DeMara, "FPGA Defragmentation for Sustainable Performance in Reconfigurable Computers," in *Proceedings of the International Symposium on VLSI*, Tampa, Florida, U.S.A., May 11 – 12, 2005.
- [8] J. Di, J. S. Yuan, and R. F. DeMara, "High Throughput Power-aware FIR Filter Design based on Fine-grain Pipeline Multipliers and Adders," in *Proceedings of the 2003 IEEE Annual Symposium on VLSI*, pp. 260 – 261, Tampa, Florida, U.S.A., February 20 – 21, 2003.
- [9] S. H. Chung, D. I. Moldovan, and R. F. DeMara, "A Parallel Computational Model for Integrated Speech and Natural Language Understanding," *IEEE Transactions on Computers*, Vol. 42, No. 10, October, 1993, pp. 1171 – 1183.
- [10] R. F. DeMara, "Parallelism, design, and performance of a marker-propagation reasoning system," Ph.D. Dissertation, University of Southern California, 1992.
- [11] J. D. Lohn, G. Larchev, and R. F. DeMara, "Evolutionary Fault Recovery in a Virtex FPGA Using a Representation that Incorporates Routing," in *Proceedings of the Seventieth International Parallel and Distributed Processing Symposium, Reconfigurable Architectures Workshop*, pp. 172, Nice, France, April 22-26, 2003.