Computer Architecture Lab's Book Citings through 2017: Group One

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Abstract — **Citations in books are summarized. The citing book is listed and the cited articles are noted. These are compiled and thus indexed for rapid identification within both printed and electronically-formatted books. By checking each of the entries, the hyperlink may be followed to view the book and cited articles. Only books which are not proceedings volumes have been included.**

Keywords — Citing article, Citation index, Cited articles, Book citations.

1.0 Introduction

In this paper, the primary focus is to identify book citations for rapid retrieval. The paper provides a concise list of them that would not otherwise be available in a single document. Sources listed were obtained via web search and then filtered as indicated below. Searches included "R.F. DeMara" and "DeMara, R" as well as "R DeMara" which were then inspected manually for matching content.

2.0 Book Citings as Indexed within Google Books

The book: Vega, Augusto, Pradip Bose, and Alper Buyuktosunoglu. Rugged Embedded Systems: Computing in Harsh Environments. Morgan Kaufmann, 2016, cited [1-10] within Chapter 4.

The book: Vega, Augusto, Pradip Bose, and Alper Buyuktosunoglu. Rugged Embedded Systems: Computing in Harsh Environments. Morgan Kaufmann, 2016, cited [11] within Chapter 5.

The book: Lerman, Gil, and Larry Rudolph. Parallel Evolution of Parallel Processors. Springer Science & Business Media, 2013, cited [12], [13] on page 127.

The book: Battezzati, Niccolò, Luca Sterpone, and Massimo Violante. Reconfigurable field programmable gate arrays for mission-critical applications. Springer Science & Business Media, 2010, cited [14] on page 174.

The book: Smith, Scott C., and Jia Di. "Designing asynchronous circuits using NULL convention logic (NCL)." Synthesis Lectures on Digital Circuits and Systems 4.1 (2009), cited [15] on page 55.

The book: Fant, Karl M. Logically determined design: clockless system design with NULL convention logic. John Wiley & Sons, 2005, cited [16] on page 286 and [17]-[18] on page 287.

The book: Nagao, Katashi. Digital content annotation and transcoding. Artech House, 2003, cited [19] on page 206.

The book: Haas, Z., et al. "Wiley Encyclopedia of Telecommunications." (2002), cited [20] on page 21.

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The book Molecular Engineering of Nanosystems, cited [33] on page 222.

The book Geller, James, Hiroaki Kitano, and Christian B. Suttner, eds. Parallel Processing for Artificial Intelligence 3. Vol. 20. Newnes, 1997, cited [34] on page 342.

The book Steven K. Rogers, Applications and Science of Artificial Neural Networks, Volume 3, cited [35] as reference 678.

3.0 Conclusion

Based on the citations above, it is possible to rapidly locate the articles by google scholar search using the stated booked titles. Citations appear on the pages as mentioned. The cited articles are listed as indicated.

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