

DANIEL MULVANEY

1629 Tiverton Street, Winter Springs, Florida, 32708
danlcmulvaney@knights.ucf.edu
407-621-1854

Objective

Currently pursuing a degree in Electrical Engineering with an established academic foundation focused on education around collaboration and communication.

Education

UNIVERSITY OF CENTRAL FLORIDA, B.S.E.E

ELECTRICAL ENGINEERING, COMPREHENSIVE TRACK

4.0 GPA - AWARDED PRESIDENT'S HONORS: FALL 2016 THROUGH FALL 2020

2016 – 2021

GRADUATION:

MAY 2021

Experience

L3Harris Technologies, Palm Bay FL

ELECTRICAL ENGINEERING INTERN

JULY 2020 – AUGUST 2020

MAY 2019 – AUGUST 2019

This position focused on Electrical Engineering systems and design, with a strong focus in Circuit Card Assembly, as well as Signal and Power Integrity.

- Followed design and testing of space-grade printed circuit boards
- Worked on FPGA Design; created Schematics to support Program Development
- Focused on the complex implementation of these devices in order to succeed in their environment
- Created and amended lab procedures in order to test devices based on their specifications

University of Central Florida, Orlando FL

NATIONAL SCIENCE FOUNDATION (NSF) RESEARCH EXPERIENCE FOR UNDERGRADS (REU)

JAN 2021 – MAY 2021

SEPT. 2019 – APRIL 2020

Research conducted alongside the University and the department, focusing on a strong parallel between knowledge gained within the classroom and that gained through self-studies working in conjunction with graduate students.

- Studied core knowledge of discrete-based logic structures and their practical applications
- Studied Post-CMOS Solutions and energy consumption

Organizations

ETA KAPPA NU – ZETA CHI CHAPTER PRESIDENT

Spring 2021

IEEE Honor society. Responsibilities include high-level management of the organization. Part of initial team reviving chapter on campus.

- Lead peer tutoring sessions
- Secure chapter recognition on campus nationally and internationally
- Establish strong communication between officers, students, and faculty

ETA KAPPA NU – ZETA CHI CHAPTER INTERNAL VICE PRESIDENT

Spring 2020 – Fall 2020

ETA KAPPA NU – ZETA CHI CHAPTER TREASURER

Spring 2019 – Spring 2020

TAU BETA PI – MEMBER

Fall 2018 – Present

Skills

Skills: Circuit Analysis, PCB design using EAGLE, KiCad, and Allegro software, Digital Systems, Electromagnetic Field Theory, Signal Analysis and Communication. ESD certified from L3Harris Technologies. Knowledge base in C, C++, Python. Satellite Communication analysis utilizing STK software.

Relevant coursework: Electronics 2, Signal Analysis/Analog Communication, Computer Architecture, Embedded Systems, Digital Signal Processing, Satellite Communication, Fundamentals of Electromagnetic Field Theory.

Projects

- Senior Design: Built and designed an integrated solution for smart home automation and sensing.
<https://github.com/UCF-SAEMS>
- Participated in a research panel along with 3 other peers. Research followed energy consumption within the MIPS architecture and sought to design energy efficient alternatives.
- Built, designed, and implemented a gyroscope utilizing a solenoid, signal conditioning circuit, microcontroller, and a tuning fork.