

ADNAN AQUIB NASEER

3417 Lariat Ln Apt 21 Orlando Florida – 32826

(408) 219-7544 / aquib.adnan@gmail.com / cal.ucf.edu/naseer.html / github.com/adnanaquib

OBJECTIVE

Energetic and Passionate engineer with excellent hardware design and software development skills

EDUCATION

Master of Science (M.S) in Computer Engineering

University of Central Florida

Advisor: Dr. Ronald DeMara

Major GPA: 3.53/4.0

Overall GPA : 3.44 / 4.0

Orlando, Florida
Aug 2013 - May 2015

Bachelor of Technology (B.S/B.Tech) in Electrical and Electronics Engineering

B S Abdur Rahman University (Crescent Engineering College)

GPA: 3.04 / 4.0

Chennai, India
Jul 2009 - Apr 2013

Relevant Coursework: FPGA Design, Multicore Programming, Full Custom VLSI, Current Topics in Parallel Processing, Advanced Computer Architecture, Complex Adaptive Systems, CMOS Digital Design, Thin Film Technology, Functional Hardware Verification

TECHNICAL SKILLS

C/C++, SystemVerilog/Verilog, Specman E, Python, HSPICE, Xilinx Vivado/ISE, ModelSim, UART/RS232, VGA, PS/2, SRAM, Virtuoso, Linux

PROFESSIONAL EXPERIENCE

Research Assistant

University of Central Florida

Orlando, Florida
Aug 2013 - Present

Thesis: Approximate Arithmetic Designs in the Presence of Process Variations and Voltage Scaling

- Designed an FPGA based Genetic Algorithm which formulated approximate multi-bit adders and lowered the power consumption of arithmetic circuits by 63 % (C++)
- Verified the functionality (Functional Coverage) of a three port router with a test bench created using UVM techniques (Specman 'e')
- Developed and analyzed delay and power consumption of various types of 16-bit Approximate Adders, the best Adder's delay and performance was found to be 35% better than the Accurate Adder (HSPICE)

Teaching Assistant

University of Central Florida

Orlando, Florida
Aug 2014 - May 2015

- Tutored and provided help on C programming (EGN 3211), MIPS assembly code (EEL 3801) and Electrical Networks (EEL 3004) to over 300 students in one year (C, MIPS Assembly Code)

Intern

ICPRO Solutions

Bangalore, India
Jun 2012 – Jul 2012

Developed automated solutions using programmable logic controllers for testing of air conditioner compressors

PUBLICATIONS

- Adnan Aquib Naseer, Rizwan A Ashraf, Damian Dechev, and Ronald F DeMara. "Designing Energy-Efficient Approximate Adders using Parallel Genetic Algorithms". SoutheastCon'15

PROJECTS

- Created an LFSR and tested its Energy Consumption, Delay and determined the optimum area and width for the circuit (Cadence Virtuoso)
- Completed and Implemented more than 10 homemade projects including a RiSC processor which was simulated on ISE, other projects involved interfacing UART , VGA , External SRAM, BRAM on the Basys2 board (Verilog)

LEADERSHIP AND VOLUNTEER EXPERIENCE

- Lead the Society of Electrical and Electronics Engineers Association to successfully increase the number of events to 6 per year (previously 2) thereby meeting the UGC standards.
- Volunteered at the IEEE Symposium Series on computational intelligence to help manage the event