

# Meghana Reddy Vangala

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## **Education:**

### **University of Central Florida**

MS Computer Engineering

**Intended grad: May 2021**

Relevant Coursework: FPGA Design, Fabrication of Solid State Devices, Semiconductor Material and Device Characterization.

### **Mahatma Gandhi Institute of Technology affiliated to Jawaharlal Nehru Technological University Hyderabad (JNTUH)**

**Percentage:70.36%**

**May 2019**

Bachelor of Technology Electronics and Communication Engineering

Relevant Coursework: Calculus- I, II, Computer programming, Switching Theory and Logic Design, Electronic Devices and Circuits, Digital Design Using Verilog HDL, Pulse and Digital Circuits, Computer Organization and Operating Systems, Electronic Measurements and Instrumentation, Linear and Digital IC Application, VLSI Design, Microprocessor and Microcontrollers, Computer Networks, Object-Oriented Programming through JAVA, Embedded Systems Design, RADAR Systems, Satellite Communications

### **Sri Chaitanya Jr. Kalasala**

**Percentage: 90.0%**

**May 2015**

High School

## **Experience:**

University of Central Florida, Graduate Research Assistant- Digital Learning

**Sept 2019-Present**

## **Trainings/Workshops:**

- Done internship in BHEL (Bharath Heavy Electronics Limited) on “Study of DCS systems”.
- Attended a 6-day workshop on “Soft Skills” by Career Path.
- Attended a number of workshops on “PCB design”, “Internet of Things” and “Raspberry Pie”.
- Attended a 2-day workshop on “Basic Telecom Technology” conducted by Regional Telecom Training Center, BSNL, Hyderabad.
- As a member of TASK (Telangana Academy for Skill and Knowledge) took part in workshops conducted by TASK like Oracle JAVA workshop.

## **Projects:**

- Electronics Corporation of India Limited, Design and Simulation of International Data Encryption Algorithm using Xilinx ISE (VHDL) –(05-18-2018 to 06-18-2018)
- Wireless Water Level Indicator using Arduino Software (IDE).
- Design of low power and low latency hybrid scheme for on-chip networks using Xilinx ISE (Verilog HDL).

## **Co-Curricular:**

- Participated in various events in our college fest every year.
- Won a consolation prize in event “Project Expo”
- Took part in Chimerax Quiz contest conducted by NIT Bhopal.

## **Extra-Curricular:**

- Placed 2<sup>nd</sup> in event “Instinct” a JAM session conducted during NIRVANA 2K16.(Our college fest)
- Volunteered an event “Project Expo” for our college fest NIRVANA 2K17.
- Organized an event “Project Expo” for our college fest NIRVANA 2K18.
- Education support volunteer in “Make a Difference” NGO.
- Organized an event “Tech Days” in our college which was initiated by **Swecha** a non-profitable student community.
- Placed 7<sup>th</sup> in National GeoMap Quiz (2012)

## **Technical Skills:**

- Languages : C,C++, Verilog HDL,VHDL.
- Softwares : Xilinx ISE, Xilinx Vivado, Multisim, MATLAB, Arduino IDE