

Mohan Krishna Gopi Krishna

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Education University Of Central Florida **Orlando, FL**
Master of Science in Computer Engineering (MSCpE) **May, 2016**
Advisor: Dr. Ronald F. DeMara **GPA: 3.16/4.0**
Research Interest : Low power Computer Architecture, Beyond CMOS computing, Spintronics

B.S.Abdur Rahman University **TN, India**
Bachelor of Technology in Electrical and Electronics Engineering (EEE) **May 2013**
Emphasis: Embedded Systems **GPA: 6.6/10**

Projects Design of Spintronic Configurable Logic Block (CLB) **Orlando, FL**
Hspice, Unix **Current-Thesis**

- Designed a sense amplifier based Magnetoresistive Look-Up Table (MLUT) and Full-Adder with clock controlled read and write.
- Design of Carbon Magnetoresistive Look-up Table (CMLUT) using CNFET.
- Comparing the performance of emerging devices to find suitable candidates to overcome limitation of CMOS technology.

Delay Uncertainty Reduction by Gate Splitting **Orlando, FL**
Hspice, Unix **Apr-May 2015**

- Reduce delay due to process variation by gate splitting.
- Adaptive implementation of threshold and supply voltage to reduce Power and Delay.
- Design an algorithm to choose the candidates suitable for optimization based on critical path.

Designed Standardized Library in Cadence **Orlando, FL**
Cadence Virtuoso, Specter, Verilog **Jan-Apr 2015**

- Designed basic cell layout of Inverter, NAND2, NOR2 and XOR2
- Designed basic cell layout of OA221, MUX 2:1, D-Flip Flop and 8x16 bit reconfigurable Linear Feedback Shift Register (LFSR).

CO-OP Branch Predictor **Orlando, FL**
SimpleScalar, Linux, C **Nov-Dec 2014**

- Increase accuracy and performance of the branch predictor.
- Use two 2-bit counter Direction Predictors each biased towards taken or not-taken.
- Use a Choice Predictor to choose between the Direction Predictor.

Defining and Computing Skeletonization of a Video **Denver, CO**
Python, Linux **Mar-May 2014**

- Reduced memory size and processing time of the video.
- Convert the video into individual frames.
- Skeletonize and recompile the video.

PV Module with Intelligent Tracking System **TN, India**
Auto-CAD, PCB Wizard, CCM compiler, PICkit2 and PIC 16F877a **Jan-May 2013**

- Attain maximum energy output from PV module.
- Tracking system using series of LDRs.
- Cleaner to remove the dust settled on the Photo Voltaic Module.

Skills
Operating Systems : Windows Family, Unix, Linux.
Programming Languages : C, C++, JAVA, Verilog, Python, MATLAB, Netlogo.
Software Packages : Windows Office, SimpleScalar, Cadence Virtuoso , Hspice, Latex

Relevant CourseWork Adv Computer Architecture, Computer Vision and Img Processing, Hardware Security
Analog and Digital CMOS Design, Semiconductor Modeling, Full Custom VLSI.

Professional Experience University of Central Florida **Orlando, FL**
Graduate Teaching Assistant **Aug 2015-Present**
EGN 3211 Engineering Analysis and Computation

C Systems Pvt. Ltd. **TN, India**
Student Internship **June-July 2012**
Embedded programming, Product testing, Debugging