

AYUSH S. PINDORIA
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PROFESSIONAL SUMMARY

Computer Engineering major at the University of Central Florida with a focus on computer networks, computer architecture, embedded systems, and FPGA design. Adept at collaborating with peers to foster a supportive community, aiming to excel in academia and the industry.

EDUCATION

B.S. in Computer Engineering **May 2023**
University of Central Florida, Orlando, FL
GPA: 3.783/4.0

Relevant Coursework: Data Structures and Algorithms, Computer Networks, and Computer Architecture.

EXPERIENCE

Undergraduate Teaching Assistant, Engineering Analysis, UCF **August 2021 – December 2021**

- Graded students' assignments and provided guidance for improvement.
- Developed solutions for coding assignments.
- Prepared lecture slides for professor's presentations.

Undergraduate Teaching Assistant, Linear Circuits I & CS I, UCF **January 2022 – July 2022**

- Conducted office hours to address students' doubts and clarify course material
- Graded students' assignments and provided guidance for improvement.
- Created solutions for quizzes.

Undergraduate Teaching Assistant, Computer Organization, UCF **August 2022 – Present**

- Conduct rebuttal sessions for exams and quizzes.
- Grade students' quizzes and exams after rebuttal and guide them about if they have any issues.

CAMPUS INVOLVEMENT

Member, CHIDA **January 2023- Present**
University of Central Florida, Orlando FL

- Work together with students of electrical, computer, and other engineering majors to discuss projects.
- Meet weekly to work on designing a 32-bit RISC V CPU's ALU.
- Attend workshops to learn about new Computer Architecture and HDL design.

SCHOLARSHIPS AND AWARDS

- The President's Honor Roll, UCF **May 2021**

SKILLS

- **Soft Skills:** Active listening, leadership, problem-solving, teamwork, strong written and verbal communication, quick learner
- **Languages:** English (Fluent), Hindi (Fluent)
- **Software skills:** Microsoft Office, C++, C, Java, Flutter, Python, Ubuntu, Git, Kali Linux
- **Hardware skills:** MIPS32/64, Verilog, embedded (TI-MSP430)